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| AIUB | **American International University- Bangladesh (AIUB)**  **Faculty of Engineering (FE)**  **Department of Electrical and Electronic Engineering (EEE)** |

**Course Project Report Outline (Digital Logic and Circuits Lab)**

**\*\*\*\* *Rename your ppt file as “Group number\_DLC LAB\_Section\_Project Report”,***

***Example: Group 01\_DLC LAB\_C\_Project Report. \*\*\****

1. **Download the template for project report writing from portal.**
2. **Title, Abstract (at least 150 words but not more than 300 words) and Keywords (3-6 keywords separated by a comma)** [3 marks]
3. **Introduction**

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| * 1. Background of Study and Motivation [1 mark] |
| * 1. Project Objectives [1 mark] |
| * 1. A brief Outline of the Report [Optional] |

1. **Literature Review *(At least 5 project-related published journal papers or conference proceedings within the year 2018 to 2023)*** [4 marks]
2. **Methodology and Modeling**

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| * 1. Introduction [1 mark] |
| * 1. Working Principle of the Proposed Project [1 mark] |
| * 1. Description of the Components [1 mark] |
| * 1. Test/Experimental Setup [1 mark] |

1. **Results and Discussions**

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| * 1. Simulation/Numerical Analysis [1 mark] |
| * 1. Measured response/Experimental Results [1 mark] |
| * 1. Comparison between Numerical and Experimental Results [1 mark] |
| * 1. Cost Analysis [1 mark] |
| * 1. Limitations in the Project [1 mark] |

1. **Conclusion and Future Endeavors** [1 mark]

**References** [1 mark]

**Appendix (if any, optional)**

**Assessment Materials and Marks Allocation:**

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| **COs** | **Assessment Materials** | **POIs** | **Marks** |
| **CO2** | Course Project Report ***(Analyze a combinational/sequential logic circuit through appropriate survey of research literature to provide valid conclusion acknowledging the limitations.)*** | **P.d.2.C4** | **20** |

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| **COs** | **Excellent to Proficient**  **[18-20]** | **Good**  **[15-17]** | **Acceptable**  **[10-14]** | **Unacceptable**  **[1-9]** | **No Response**  **[0]** | **Secured Marks** |
| **CO2**  **P.d.2.C4** | The outcome of the project demonstrates a course project using logic ICs, transistors, switches, display devices, etc. that can solve a complex engineering problem in the electrical and electronic engineering discipline through appropriate research. | The outcome of the project somewhat demonstrates a course project using logic ICs, transistors, switches, display devices, etc., and also somewhat solves a complex engineering problem in the electrical and electronic engineering discipline through some research. | The outcome of the project demonstrates a course project using logic ICs, transistors, switches, display devices, etc. but cannot solve a complex engineering problem properly in the electrical and electronic engineering discipline through appropriate research. | The outcome of the project does not demonstrate a course project using logic ICs, transistors, switches, display devices, etc. also could not solve a complex engineering problem in the electrical and electronic engineering discipline through appropriate research. | No Response at all |  |
| **Comments** |  |  |  |  | **Total Marks (20)** |  |